Jen- purpose of this semester is to get the most challenging/ critical step done

* Wake up, transmit based on power availability
* Robby+Nathaniel- cannot include bluefruit in final bc it’s a terminal, won’t do chaining (cannot initiate a connect)
* Puts us back at sq 1 for next semester if we gotta migrate
* Power concerns: bluefruit has to be idle and wait for master, vs if the SiP can initiate connection
* Might be worth it to say in the future you want it to be able to chain
* Nathaniel- think about it
* RN- GW says its a key objective
* JH- it might be unreasonable given other constraints, we set out goalposts
* JH- first prototype is figuring out which chips are used, get prototype software 90% done
* Next prototype is designing PCB, modding code and testing rigorously
* Don’t want to have much coding intensive
* Want to be making plans based on having to change components. Or setting us up to not switch?
* Nathaniel- might be a small coding problem, but it’s hard to say (hopefully not more than a week to change components)
* Robby- will keep looking to see if anything viable, switch over if we see fit

Working based on bluefruit still

Ideally, this semester is “get the code done” for each individual subsystem

Start tying the systems together

Power system interfacing with MSP430

* Send an interrupt to MSP430 to wake up bluefruit, but Vdd to bluefruit whenever it’s not transmitting
* Main control in MSP430, and this controls switch to Vdd on bluefruit
* PMIC responsibilities: 1) regulate vdd to bluefruit, 2) tell msp430 what we can do

Documentation Party Friday 1-5pm. Be there or be square.

Don’t over commit us to ANYTHING